

<b>Notice of References Cited</b>	Application/Control No. 10/055,470	Applicant(s)/Patent Under Reexamination LITWIN, ET AL.	
	Examiner Stephen M. Baker	Art Unit 2133	Page 1 of 1

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-5,027,357	06-1991	Yu et al.	714/758
*	B	US-5,396,502	03-1995	Owsley et al.	714/784
*	C	US-5,490,154	02-1996	Mester, Roland	714/784
*	D	US-5,563,896	10-1996	Nakaguchi, Yukimi	714/785
*	E	US-5,971,607	10-1999	Im, Yong-Hee	714/784
*	F	US-6,031,875	02-2000	Im, Yong-Hee	375/262
*	G	US-2003/0140303 A1	07-2003	Litwin et al.	714/785
*	H	US-2003/0192007 A1	10-2003	Miller et al.	714/782
*	I	US-6,651,214	11-2003	Weng et al.	714/771
	J	US-			
	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Oh, K.; et al., "An Efficient Reed-Solomon Decoder VLSI with Erasure Correction", 1997 IEEE Workshop on Signal Processing Systems, pp. 193-201.
	V	
	W	
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.